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Jianbin Wei; Cheng-Zhong Xu;  
*Computers, IEEE Transactions on*  
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*Distributed Computing Systems, 1996., Proceedings of the 16th International Conference on*  
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Ha, V.A.; Musliner, D.J.;  
*System Sciences, 2003. Proceedings of the 36th Annual Hawaii International Conference on*  
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Dube, R.; Rais, C.D.; Tripathi, S.K.;  
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Sudha Krishnamurthy; Sanders, W.H.; Cukier, M.;  
*Parallel and Distributed Systems, IEEE Transactions on*  
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Peter M. Chen, David A. Patterson

 June 1993 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1993 ACM SIGMETRICS conference on Measurement and modeling of computer systems SIGMETRICS '93**, Volume 21 Issue 1

Publisher: ACM Press

 Full text available: [pdf\(1.36 MB\)](#)

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Current I/O benchmarks suffer from several chronic problems: they quickly become obsolete, they do not stress the I/O system, and they do not help in understanding I/O system performance. We propose a new approach to I/O performance analysis. First, we propose a self-scaling benchmark that dynamically adjusts aspects of its workload according to the performance characteristic of the system being measured. By doing so, the benchmark automatically scales across current and future systems. The eval ...

2 [OPTNET: a cost-effective optical network for multiprocessors](#)



Enrique V. Carrera, Ricardo Bianchini

 July 1998 **Proceedings of the 12th international conference on Supercomputing ICS '98**

Publisher: ACM Press

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3 [Adaptive resource management for flow-based IP/ATM hybrid switching systems](#)



Hao Che, San-qi Li, Arthur Lin

 October 1998 **IEEE/ACM Transactions on Networking (TON)**, Volume 6 Issue 5

Publisher: IEEE Press

 Full text available: [pdf\(570.85 KB\)](#)

 Additional Information: [full citation](#), [references](#), [index terms](#)

**Keywords:** adaptive resource management, cut-through switching, flow cache management, flow classification, flow-based IP/ATM hybrid switching

4 [CRL: high-performance all-software distributed shared memory](#)



K. L. Johnson, M. F. Kaashoek, D. A. Wallach

 December 1995 **ACM SIGOPS Operating Systems Review , Proceedings of the fifteenth**

**ACM symposium on Operating systems principles SOSP '95, Volume 29**  
 Issue 5

**Publisher:** ACM Press

Full text available: [pdf\(2.02 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

**5 Simulation and implementation issues: DIRAC: a software-based wireless router system**

Petros Zerfos, Gary Zhong, Jerry Cheng, Haiyun Luo, Songwu Lu, Jefferey Jia-Ru Li  
 September 2003 **Proceedings of the 9th annual international conference on Mobile computing and networking MobiCom '03**

**Publisher:** ACM Press

Full text available: [pdf\(385.84 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Routers are expected to play an important role in the IP-based wireless data network. Although a substantial number of techniques have been proposed to improve wireless network performance under dynamic wireless channel conditions and host mobility, a system support framework is still missing. In this paper, we describe DIRAC, a software-based router system that is designed for wireless networks to facilitate the implementation and evaluation of various channel-adaptive and mobility-aware protoc ...

**Keywords:** distributed router architecture, wireless network

**6 Modelling the Internet: A tool for RApid model parameterization and its applications**

Kun-chan Lan, John Heidemann

August 2003 **Proceedings of the ACM SIGCOMM workshop on Models, methods and tools for reproducible network research MoMeTools '03**

**Publisher:** ACM Press

Full text available: [pdf\(373.54 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

The utility of simulations and analysis heavily relies on good models of network traffic. However, it is difficult to model and simulate the Internet traffic because of the network's great heterogeneity and rapid change. The statistical properties of Internet traffic not only constantly change over time but also vary in other dimensions such as locations and directions. Previously we have developed a tool *RAMP* that supports rapid parameterization of traffic models from live network measur ...

**7 A QoS-Provisioning neural fuzzy connection admission controller for multimedia high-speed networks**

Ray-Guang Cheng, Chung-Ju Chang, Li-Fong Lin

February 1999 **IEEE/ACM Transactions on Networking (TON)**, Volume 7 Issue 1

**Publisher:** IEEE Press

Full text available: [pdf\(342.90 KB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

**8 Dynamic leveling: adaptive data broadcasting in a mobile computing environment**

Wen-Chih Peng, Jiun-Long Huang, Ming-Syan Chen

August 2003 **Mobile Networks and Applications**, Volume 8 Issue 4

**Publisher:** Kluwer Academic Publishers

Full text available: [pdf\(185.70 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The research issue of broadcasting has attracted a considerable amount of attention in a mobile computing system. By utilizing broadcast channels, a server is able to continuously and repeatedly broadcast data to mobile users. From these broadcast channels, mobile users obtain the data of interest efficiently and only need to wait for the required data to be present on the broadcast channel. Given the access frequencies of data items, one can design proper data allocation in the broadcast channe ...

**Keywords:** broadcast disks, broadcast programs, mobile computing, multiple broadcast channels

**9 A cost effective architecture for vectorizable numerical and multimedia applications**

 Francisca Quintana, Jesus Corbal, Roger Espasa, Mateo Valero

July 2001 **Proceedings of the thirteenth annual ACM symposium on Parallel algorithms and architectures SPAA '01**

**Publisher:** ACM Press

Full text available:  pdf(293.82 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper analyzes the performance of vector-dominated regions of code in numerical and multimedia applications in a superscalar+vector architecture and compares it to an 8-way superscalar processor. The ability to split a program's execution into scalar and vector regions allows us to show that (1) as expected, the vector unit is much better than the wide issue superscalar at executing the vector-dominated regions of the code; (2) on the scalar regions, the 8-way superscalar, although bette ...

**10 Wireless LAN optimizations: Improving protocol capacity with model-based frame**

 scheduling in IEEE 802.11-operated WLANs

Hwangnam Kim, Jennifer C. Hou

September 2003 **Proceedings of the 9th annual international conference on Mobile computing and networking MobiCom '03**

**Publisher:** ACM Press

Full text available:  pdf(480.66 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

In this paper, we develop a model-based frame scheduling scheme, called *MFS*, to enhance the capacity of IEEE 802.11-operated wireless LANs (WLANs). In MFS each node estimates the current network status by keeping track of the number of collisions it encounters between its two consecutive successful frame transmissions, and, based on the estimated information, computes the current network utilization. The result is then used to determine a scheduling delay that is introduced (with the ...

**Keywords:** IEEE 802.11, performance analysis, protocol enhancement, wireless LANs (WLANs)

**11 Placement: Parallel placement for field-programmable gate arrays**

 Pak K. Chan, Martine D. F. Schlag

February 2003 **Proceedings of the 2003 ACM/SIGDA eleventh international symposium on Field programmable gate arrays FPGA '03**

**Publisher:** ACM Press

Full text available:  pdf(91.86 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Placement and routing are the most time-consuming processes in automatically synthesizing and configuring circuits for field-programmable gate arrays (FPGAs). In this paper, we use the negotiation-based paradigm to parallelize placement. Our new FPGA placer, NAP (Negotiated Analytical Placement), uses an analytical technique for coarse placement and the negotiation paradigm for detailed placement. We describe the serial algorithm and report results. We also report findings related to parallelizi ...

**Keywords:** FPGAs, analytical placement, parallel placement, timing-driven placement

**12 SorTables: a browser for a digital library**

 William C. Wake, Edward A. Fox

December 1995 **Proceedings of the fourth international conference on Information and knowledge management CIKM '95**

Publisher: ACM Press

Full text available:  pdf(769.53 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

13 High speed converters, amplifiers, and low power analog circuits: Low-voltage low-power high dB-linear CMOS exponential function generator using highly-linear V-I converter

Quoc-Hoang Duong, Trung-Kien Nguyen, Sang-Gug Lee

August 2003 **Proceedings of the 2003 international symposium on Low power electronics and design ISLPED '03**

Publisher: ACM Press

Full text available:  pdf(223.37 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A CMOS voltage-to-current converter with exponential characteristic is presented in this paper. The concept of Taylor series expansion is used for realizing the exponential characteristic. The proposed exponential V-I converter is composed of a current-to-current squarer and a linear V-I converter with the use of linearization technique. Based on a 0.25 mm CMOS process, simulations show a 23 dB of linear-output current range and the linearity within 20 dB with error less than  $\pm 0.5\text{dB}$  is ach ...

**Keywords:** VGA, dB-linear, exponential V-I converter

14 Syntactic accidents in program analysis: on the impact of the CPS transformation

 Daniel Damian, Olivier Danvy

September 2000 **ACM SIGPLAN Notices , Proceedings of the fifth ACM SIGPLAN international conference on Functional programming ICFP '00**, Volume 35 Issue 9

Publisher: ACM Press

Full text available:  pdf(264.32 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We show that a non-duplicating CPS transformation has no effect on control-flow analysis and that it has a positive effect on binding-time analysis: a monovariant control-flow analysis yields equivalent results on a direct-style program and on its CPS counterpart, and a monovariant binding-time analysis yields more precise results on a CPS program than on its direct-style counterpart. Our proof technique amounts to constructing the continuation-passing style (CPS) counterpart of flow information ...

**Keywords:** CPS transformation of binding-time information, CPS transformation of control-flow information, binding-time improvements, continuation-based evaluation, continuation-based partial evaluation

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BEA WebLogic Server Release 7.0 Documentation :: Programming WebLogic JDBC. ...

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Click here for information about our SQL Server Performance Tuning Workshop. ... Why do I need **Performance Tuning**? Faster **transactions** - improved ...

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## **JDBC transaction performance tips**

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## Netscape Directory Server Administrator's Guide: Chapter 14 Tuning ...

For a better understanding of how these **parameters** impact your **server's search performance**, see "About Indexes". **Tuning Database Performance** ...

[www.redhat.com/docs/manuals/dir-server/ag/6.2/dsmanage.htm](http://www.redhat.com/docs/manuals/dir-server/ag/6.2/dsmanage.htm) - 29k

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manner can result in poor **performance** or **transaction** rollbacks. .... **parameters** for the **IBM HTTP Server** work well for small and medium configurations, ...

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**P A R A M E T E R . T U N I N G.** Directory **Server Tuning**. All of our tests used **IBM** **Directory Server** 5.1, running on iSeries, as the directory **server**. ...

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## WebLogic Server Performance Tuning @ WEBLOGIC JOURNAL

This **parameter** has been renamed as **cache-between-transactions** in WebLogic ....

Additional information regarding WebLogic **Server performance tuning** is ...

[wldj.sys-con.com/read/42887.htm](http://wldj.sys-con.com/read/42887.htm) - 84k

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S27	13	717/127.ccls. ((tuning or performance) near9 parameter) transaction time	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/24 06:37
S26	11	717/127.ccls. ((tuning or performance) near4 parameter) transaction response	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/24 06:37
S25	35	717/127.ccls. ((tuning or performance) near4 parameter)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/24 06:36
S7	131	((monitor\$4 (tunning or tune\$2)) near8 (system or server)) ((processor or cpu) same memory same (usage or consum\$6 or balance or bias or ajust\$4 or between or set\$2 or setting\$2)) ((transaction or operation) near2 time) (respons\$4 near2 time) ((log\$2 or logger or logging) with (file or record or data or database))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/24 06:35
S24	10	709/203.ccls. ((transaction response) near4 time) monitor\$4 (tuning with parameter)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/24 06:02
S23	19	709/203.ccls. ((transaction response) near4 time) monitor\$4 tuning parameter	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/24 06:01
S22	21	709/203.ccls. ((transaction response) near4 time) monitor\$4 tuning	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/24 06:01
S19	91	709/224.ccls. ((server or system) near6 (transaction or tuning)) tuning performance parameter	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/24 06:00
S21	29	709/224.ccls. ((server or system) near6 (transaction or tuning or monitor\$4)) tuning performance parameter ((response transaction) near4 time)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/24 05:53

## EAST Search History

S20	152	709/224.ccls. ((server or system) near6 (transaction or tuning or monitor\$4)) tuning performance parameter response	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/24 05:52
S18	57	709/224.ccls. ((server or system) near4 transaction) tuning performance parameter	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/24 05:52
S17	67	709/224.ccls. ((server or system) near4 transaction) tuning	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/24 05:51
S16	791	709/224.ccls. ((server or system) near4 transaction)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/24 05:51
S15	36	((tun\$2 or tuning) monitor\$4) same (processor or cpu or memory or parameter or server) (time near6 (respons\$4 transaction)) (time\$1out or (time adj out)) (server same (transaction or respons\$4)) ((processor or cpu) same memory same (set\$2 or setting or balanc\$4 or bias or ajust\$4 or parameter or allocat\$4 or tun\$2 or tuning or between)) (parameter near6 (tun\$2 or tuning or monitor\$4 or set\$2 or setting\$2))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/23 08:03
S14	44	((tun\$2 or tuning) monitor\$4) same (processor or cpu or memory or parameter or server) (time near6 (respons\$4 transaction)) (time\$1out or (time adj out)) (server same (transaction or respons\$4)) ((processor or cpu) same memory same (set\$2 or setting or balanc\$4 or bias or ajust\$4 or parameter or allocat\$4 or tun\$2 or tuning or between)) parameter	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/23 07:56
S13	47	((tun\$2 or tuning) monitor\$4) same (processor or cpu or memory or parameter or server) (time near6 (respons\$4 transaction)) (time\$1out or (time adj out)) (server same (transaction or respons\$4)) ((processor or cpu) same memory same (set\$2 or setting or balanc\$4 or bias or ajust\$4 or parameter or allocat\$4 or tun\$2 or tuning or between))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/23 07:56
S12	164	((tun\$2 or tuning) monitor\$4) same (processor or cpu or memory or parameter or server) (time near6 (respons\$4 transaction)) (time\$1out or (time adj out)) (server same (transaction or respons\$4)) ((processor or cpu) same memory)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/23 07:54
S11	185	((tun\$2 or tuning) monitor\$4) same (processor or cpu or memory or parameter or server) (time near6 (respons\$4 transaction)) (time\$1out or (time adj out)) (server same (transaction or respons\$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/23 07:54

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S10	104	((tun\$2 or tuning) monitor\$4) same (processor or cpu or memory or parameter) (time near6 (respons\$4 transaction)) (time\$1out or (time adj out))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/23 07:51
S9	240	((tun\$2 or tuning) monitor\$4) same (processor or cpu or memory or parameter) (time near6 (respons\$4 transaction))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/23 07:50
S8	21	((monitor\$4 (tunning or tune\$2)) near8 (system or server)) ((processor or cpu) same memory same (usage or consum\$6 or balance or bias or ajust\$4 or between or set\$2 or setting\$2)) ((transaction or operation) near2 time) (respons\$4 near2 time) ((log\$2 or logger or logging) with (file or record or data or database)) ((tunning or tune\$2 or monitor\$4 or set\$2 or setting\$2) near2 parameter)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/23 07:27
S6	166	((monitor\$4 (tunning or tune\$2)) near8 (system or server)) ((processor or cpu) same memory same (usage or consum\$6 or balance or bias or ajust\$4 or between or set\$2 or setting\$2)) ((transaction or operation) near2 time) (respons\$4 near2 time) (log\$2 or logger or logging)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/23 07:15
S5	249	((monitor\$4 (tunning or tune\$2)) near8 (system or server)) ((processor or cpu) same memory same (usage or consum\$6 or balance or bias or ajust\$4 or between or set\$2 or setting\$2)) ((transaction or operation) near2 time) (respons\$4 near2 time)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/23 07:14
S4	356	((monitor\$4 (tunning or tune\$2)) near4 (system or server)) ((processor or cpu) same memory same (usage or consum\$6 or balance or bias or ajust\$4 or between or set\$2 or setting\$2)) ((transaction or operation) near2 time)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/23 07:14
S3	6432	((monitor\$4 or tunning or tune\$2) near4 (system or server)) ((processor or cpu) same memory same (usage or consum\$6 or balance or bias or ajust\$4 or between or set\$2 or setting\$2)) ((transaction or operation) near2 time)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/23 07:13
S2	24225	((monitor\$4 or tunning or tune\$2) near4 (system or server)) ((processor or cpu) same memory same (usage or consum\$6 or balance or bias or ajust\$4 or between))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/23 07:09
S1	76144	((monitor\$4 or tun\$4) near4 (system or server)) ((processor or cpu) same memory)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/23 07:07